

Minutes – Las Vegas, NV – October 10, 2003

The DCC Working Group meeting was held in the Sands Convention Center from 7:30 pm –11:00 pm on Friday, October 10th, in Room 3003.

Attendees:

Brian Barnt – NMRA L5192
Stan Ames – NMRA L5357
Peter Ely – NMRA L5564
Zana Ireland – Digitrax
AJ Ireland – Digitrax
Jim Scorse – NCE Corp
Bernd Lenz – Lenz Elektronik

1. Review of Summer Meeting

2. Topic 9910241 – Analog Output Instruction

The Analog Topic was discussed, and a consensus reached to start a ten-week review, and move forward without a demonstration of this new instruction. While reviewing the minutes from the Summer 2002 meeting, it was found that the following was already approved by the Working Group:

Instruction is a subset of the *Advanced Operation Instruction* group and is three bytes in length as follows:

00111101 0 VVVVVVVV 0 DDDDDDDD

where:

VVVVVVVV = Analog Function Vector (0-255)

DDDDDDDD = Analog function data (0-255)

Discussion centered around the need for this function. Values for VVVVVVVV will be assigned as needed by the Working Group, but otherwise reserved. Value 00000001 is defined for Volume control, others are reserved.

3. Topic 0107122 – User Defaults

Waiting for demonstration.

4. Topic 0104191 – CV Table Structure Proposal

Status: Waiting for demonstration. Experimental CVs assigned as follows:

Storage Block B0 – Address Pointer	129	E			Y	Spring 2002
Storage Block B0 – Data Register	130	E			Y	Spring 2002
Storage Block B1 – Address Pointer	131	E			Y	Spring 2002
Storage Block B1 – Data Register	132	E			Y	Spring 2002
Storage Block B2 – Address Pntr MSB	133	E			Y	Spring 2002
Storage Block B2 – Address Pntr LSB	134	E			Y	Spring 2002
Storage Block B2 – Data Register	135	E			Y	Spring 2002
Storage Block B3 – Address Pntr MSB	136	E			Y	Spring 2002
Storage Block B3 – Address Pntr LSB	137	E			Y	Spring 2002
Storage Block B3 – Data Register	138	E			Y	Spring 2002

5. Topic 0304231 - Additional Mfg CVs for Accessory Decoders

David Nickolson of ZTC has proposed adding CVs 542-544 to the Manufacturer Unique area starting at CV 545. [There was a general consensus to accept this proposal at the Summer 2003 meeting as well as this one.](#)

6. Topic 0305001– Medium Plug Enhancement

Roco has several different proposals to expand the Medium connector in locomotives to support more functions. S-Helper Service has also enhanced the medium plug, which needs to be investigated.

- A counter-proposal was discussed to include a function interface (ala SUSI)
- More information is needed about pin assignments
- A suggestion was made to use a second connector for functions
- A further suggestion was made to use a JST connector for additional functions (not the 9-pin in current use), and that this connector should include a serial interface

7. Topic 0307121 – New Decoder Register Matrix

Rutger Friberg has proposed changing the way CVs are accessed and organized. Reinhard Mueller to coordinate discussion.

8. Topic 0305051 – Decoder Lock Proposal

AJ Ireland of Digitrax posted a proposal (attached) for a new method of programming multiple decoders in a single locomotive. This topic was discussed, and several modifications are interspersed with AJ original proposal (see the *italic* text)

9. Topic 0307122 – Service Mode Decoder Lock Proposal

Bernd Lenz, on behalf of Bachmann, is proposing an additional Service Mode instruction to prevent the programming of decoders in low-end systems where there is no programming track. Using this method would eliminate the problem of re-programming every decoder on the layout to the same address. This proposal also eliminates a conflict between RP-9.2.1 and RP-9.2.3.

The service mode instruction (0xF9) would be modified to match the format defined in RP-9.2.1.

RP-9.2.3, Appendix A defines the 0xF9 command as follows:

```
long-preamble 0 AAAAAAAAA 0 11111001 0 EEEEEEEEE 1
```

RP-9.2.1 defines all Configuration Variable access instructions as having the overall format:

```
preamble 0 AAAAAAAAA 0 1111CCCC 0 DDDDDDDD 0 EEEEEEEEE 1
```

To resolve both the problem and the document conflict, Bernd proposes the following broadcast version of the packet:

Upon receiving an 0xF9 command, a decoder which implements this feature will check it's short address against the address contained in the packet (see below). If the address **does not** match the address in the packet, the decoder will ignore further service mode commands (even after a power cycle).

If the decoder receives any packet that is not a valid service mode packet, it will stop ignoring service mode packets.

Existing decoders that implement the existing 0xF9 would reject the packet because the checksum will not match.

The proposed sequence of events would be:

1. <User selects an existing locomotive address & enters programming mode>
2. The system broadcasts the 0xF9 instruction along with the address entered, and turns off power to the track.
3. The command station indicates that it is ready for a new locomotive to be placed on the track, or that an existing locomotive can be re-programmed.
4. <User places new locomotive on the track.>
5. <User selects new address to program (or reprogram) the locomotive to.>
6. The command station sends the normal service mode sequence to program the address.

The format of the instruction is:

```
111111111111 0 00000000 0 11111001 0 0aaaaaaa 0 EEEEEEEEE 1
```

where:

```
aaaaaaa      – short address of a decoder to be programmed
EEEEEEEE    -- Error Byte
```

[This topic is under 12 week review.](#)

10. Topic 0307161 - Clarification of sequence of Multiple Protocols

An additional comment was received about command stations and decoders supporting more than two protocols (i.e. DCC, Bi-Directional, Motorola), and what order that they should be in. This information needs to be clarified – especially for new manufacturers.

[Brian to investigate before summer meeting.](#)

11. Topic 0307162 - Transition Sequence for Multiple Protocols

As a corollary to Topic 0307161, a comment was received about adding a possible transition sequence before changing to an alternative protocol in multi-protocol systems. An separate proposal was received to add a requirement in S-9.2 that all multi protocol operations end with the second half of a zero bit. This improves backward compatibility and resynchronizes the DCC signal.

12. Topic 0307163 - Glossary

A proposal was received to convert the Draft Glossary document to a Technical Note. There was a short discussion and general consensus to do this.

[Approved .](#)

13. Topic 0307164 – RP-9.3.2 Format Revision

[Withdrawn.](#)

14. Topic 0307165 – Clarification / Rewording of RP-9.2.1 and RP-9.2.2

Many comments were received about the complexity of the new instructions in RP-9.2.1. Stan review the wording. [Tabled till summer \(waiting on Topic 305051\).](#)

15. Topic 0309301 – RP-9.3.1 Revision

Proposal to change transmission rate in RP-9.3.1 from 125kbs to 250kbs.

[This change would make the broadcast portion available in every packet window. There was some discussion, but more testing is required.](#)

16. Topic 0309302 – RP-9.3.2 Revision

Proposal to open up a second optional transmission window in RP-9.3.2 for broadcast. Window would proceed normal window and be totally optional.

[No proposal was ready for review. Tabled till summer.](#)

No further action was taken on the following topics. These topics were **not** discussed at the Las Vegas meeting, and have been included here so further action can be taken by the Working Group.

17. Topic 0106171 – Equipment Control Instruction

This topic was originally discussed at the St. Louis meeting in the Summer of 2001. It was subsequently discussed and modified at the meeting in Durango in the Fall of 2002.

17.1. Topic 02062001 - Decoder Identity

Bob Jacobsen's report on Decoder Identity was discussed thoroughly at the Durango meeting in the Fall of 2002. Bob's proposal was modified and accepted as follows.

CV 8 – Manufacturer ID

CV 109 (a) – Decoder Type (SW features and CVs supported)

CV 110 (b) – Model Number (HW features)

CV 111 (c) – Build Number (HW or SW)

CV 7 – for compatibility, it must contain the same value as one of the following: CV a, CV b, CV c.

Summary:

DCC users sometimes want to install more than one decoder in a single locomotive. Common cases are: using separate decoders for motor control and sound generation, and also additional function only decoders. Because these decoders often need to have their CVs adjusted separately, a mechanism is needed to communicate with only one of multiple decoders installed in a locomotive shell, at a time. As a common workaround, it is possible to have several decoders at different CV1 or CV17/18 addresses, but this is cumbersome to operate and is problematic with service mode programming.

This Lock mechanism must be able to:

- 1) Do service-mode read and write from a specific decoder, with no need to disconnect other decoders electrically.
- 2) Discover the type of decoder(s) present inside a locomotive, without disassembly, and despite multiple decoders being present.
- 3) Work with existing DCC systems without modification

Proposal: Using CV15 and CV16 formally assigned by NMRA to Digitrax for Experimental Lock feature development.

CV16 will carry a number from 0 to 7 inclusive. This is called the "**ID number**". This number identifies a single decoder, so a unique value must be assigned to each decoder in a particular locomotive.

Because most users will use this mechanism with decoders that provide different functions, the recommended coding is:

0: Reset value, as shipped

~~255: Broadcast ID so all decoders can be programmed to the same address at the same time.~~

This was seen as an unnecessary complication.

~~De-facto~~ Definitions:

- 1: Motor decoder
- 2: Sound decoder
- 3: Function-only decoder (e.g. for additional lights)
- 4-7: Reserved for other user choices

Values above 7 cannot be programmed.

CV15 is the "**unlock number**", 0 to 7 inclusive, and is used to select the decoder that will respond to standard NMRA programming commands in Service and Operations mode programming.

Lock Rule:

When the values in CV15 and CV16 are equal, all CVs in the decoder can be read or written. When the values in CV15 and CV16 are not equal, only CV15 can be written or read.

Some manufacturers may want to provide a bit in a CV to enable/disable this feature. If so, that should be in a CV reserved for the manufacturers use, as no NMRA CV has been reserved for this purpose.

[The NMRA will define an optional CV bit for this purpose.]

Note that a command to reset the decoder to default CV values must not be acted upon unless the CV15 and CV16 values are equal. This prevents the user from accidentally resetting multiple decoders at the same time, and losing the ability to separately address them.

CV 1 cannot be locked for writing. This way a user can regain control of a locked decoder.

Proposed Alternative Method: Cycle all values for CV15 to regain control of a locked decoder.

To configure decoders at new installation:

Before installing each decoder in the locomotive:

- a) Connect it to the programming track all by itself
- b) Write 8 to CV8 to ensure CV15 and CV16 are both zero
- c) If desired, read CV15 to confirm that the decoder can be addressed (This is a read of 0, hence fast)
- d) Write CV16 to the desired ID number, e.g. 2
- e) Write CV15 to the ID number
- f) Configure the rest of the decoder
- g) Disconnect it and install it in the locomotive

Repeat in turn for each decoder to be installed

Note that if the user is installing only one decoder, there is no need to modify the default values of zero in CV15 and CV16.

There was a detailed discussion about the use of 0 as a 'master' key to program all decoders (sub-ids) to the same value.

Jim Scorse of NCE volunteered to test this concept & report back.

To access a decoder after installation:

- a) Write the ID of the desired decoder into CV15
- b) Read or write other CVs as needed, that will now be executed by the decoder with a matching value in CV16

To identify the decoder(s) present in a locomotive:

- a) Write a 0 to CV15
- b) Attempt to read a 0 from CV16.
- c) If no acknowledge is received, there is no decoder with ID 0. If an acknowledge is received, a decoder with ID 0 exists.

Repeat this process for ID 1 through 7 to check for those.

Because the ID values are low and only use the 3 least significant bits, a successful read is quite fast. An unsuccessful read, i.e. when the decoder is not present, will take some time on many existing command stations, but eventually give an answer. This can be made faster in the future by having command stations attempt to read just the expected value, instead of 0 to 255, in the absence of an acknowledge.

When a mixture of lockable and "old" non-lockable decoders are present, simply program the lockable decoder(s) and then finally program the "old" decoder. Operations mode programming is effected with the same logic and makes configuration and programming very easy.

Conclusion: This proposal meets all the required and desired properties of a usable decoder lock mechanism. Of importance is the fact that it works with existing decoders and command stations. Many other methods with greater levels of complexity, flags, sophistication and problems were evaluated, but did not compare to the practicality of this minimalist approach.

Additionally, this has been field-tested in many thousands of decoders of different types over the last 18-months and is separately supported by more than one decoder and software manufacturer. The firmware impact in the decoders is minimal, and is compact and easily added to existing programming algorithms in memory-limited CPU's.

AJ Ireland @ Digitrax Inc.

PS Special thanks to all the users and others who helped to extensively field-test this over the past 18 months.

CV New Decoder Register Matrix Proposal

Gothenburg, July 7, 2003, R.Friberg

Addr	St V	Acc	Brke	V-Max	V-Mid	Version	Mfg ID	PWM rate	EME Cutout	decoder adj.	Ext Addr	Ext Addr	Consist	Config	49-64	67-94	Base Block	Extended Block	Mfg Info Block	Mfg Unique Block	Speed Curve Block	NMRA reserved block	
1																							
2																							
3																							
4																							
5																							
6																							
7																							
8																							
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29																							
49-64																							
67-94																							
																		Base Block	Extended Block	Mfg Info Block	Mfg Unique Block	Speed Curve Block	NMRA reserved block

CORE Config Register